# **ELECTRICAL ENGINEERING**

## PAPER-I

Time Allowed: **Three Hours** 

Maximum Marks: 300

### **Question Paper Specific Instructions**

Please reach each of the following instruction carefully before attempting questions:

There are **EIGHT** questions divided in **TWO** sections.

Candidate has to attempt **FIVE** questions in all

Questions **No.1** and **5** are **compulsory** and out of the remaining, any **THREE** are to be attempted choosing at least **ONE** question from each section.

The number of marks carried by a question/part is indicated against it.

Wherever any assumptions are made for answering a question, they must be clearly indicated.

Diagrams/Figures, wherever required, shall be drawn in the space provided for answering the question itself.

Unless otherwise mentioned, symbols and notations carry their usual standard meanings.

Attempts of questions shall be counted in sequential order. Unless struck off, attempt of a question shall be counted even if attempted partly.

Any page of portion of the page left blank in the Question-cum-Answer Booklet must be clearly struck off.

Answers must be written in **ENGLISH** only.



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### SECTION-A

1. (a) (i) Obtain the partial differential equation governing the equations

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f(u, v) = 0, u = x + yz, v = x + y + z

- (ii) Construct a partial differential equation of all surfaces of revolution having z-axis as the axis of rotation.
  - [12 Marks]
- (b) Discuss the phenomena of Hysteresis in ferromagnetic materials. On the B-H curve show the retentivity, coercivity and saturation points. What is coercive force and the energy dissipated per unit volume of the ferromagnetic substance during the hysteresis cycle?

[12 Marks]

(c) Explain and derive continuity of current equation using the principle of conservation of charge.

[12 Marks]

(d) Using the principle of superposition, determine the current I in the 5 $\Omega$  resistor in the circuit shown in the figure.



[12 Marks]

(e) State Ampere's circuital law. A hollow conducting cylinder has inner radius a and outer radius b and carriers current I along the positive z-direction. Find  $\vec{H}$  everywhere.

[12 Marks]





(b) For the circuit shown in the figure given below, the total current I entering the circuit is  $30.0 \angle -21^{\circ}A$ . Determine the voltage V and the branch currents I<sub>2</sub> and I<sub>4</sub>.



[20 Marks]



5.

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(c) Show that the ratio of the amplitudes of conduction current density and displacement current density is  $\frac{\sigma}{\sigma c}$  for the applied field.  $E = E_m \sin \omega t$ . Assume  $\mu = \mu_0$ . What is the amplitude ratio if the applied field is  $E = E_m e^{-t/\tau}$ , where t is real? [10 Marks] **SECTION-B** (a) (i) Using an iterative method, write C program segment to generate first  $n(n \ge 8)$  Fibonacci numbers. (ii) Find minimal disjunctive normal form (DNF) for the expressions  $\overline{x}yz + xyz + xy\overline{z}$  and  $x + yz + z\overline{x}y + \overline{y}xz$ . [12 Marks] (b) Why and how are volt-ratio boxes utilized along with d.c. potentiometers? How should the value of the volt-ratio box resistance be chosen? [12 Marks] (c) The self-capacitance or distributed capacitance of a coil is measured using Q meter. The first measurement is carried out at 2.5 MHz, when the tuning capacitor is set at 425 pF. The second measurement is carried out by increasing the frequency to 6 MHz, when the tuning capacitor is set at 60 pF. Determine the distributed capacitance of the coil.

[12 Marks]

(d) Calculate the output voltage  $V_0$  at 7 ms in the figure shown below if a ±10V square wave of 250Hz source is applied to  $R = 10\Omega$ ,  $C = 20\mu$ F. The diode is ideal and capacitor is initially uncharged.



[12 Marks]

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(e) Determine  $V_{GS}$  and  $V_{DS}$  for the E-MOSFET circuit shown in the figure below. The minimum values of  $I_{D(ON)} = 200 \text{ mA}$  at  $V_{GS} = 4V$  and Gate to Source threshold voltage  $V_{GS(Th)} = 2V$ .





(c) (i) Find the voltage gain of JFET amplifier shown in the figure below for the drain to source current with gate shorted,  $I_{DSS} = 10 \text{ mA}$ , cut-off voltage  $V_{GS(OFF)} = -4V$  and  $I_D = 2\text{mA}$ . If a load resistance of 4.7 k $\Omega$  is a.c. coupled to the output of this amplifier, calculate the percentage change in voltage gain.



(ii) Find  $R_B$  in the figure shown below for silicon transistor with  $\beta = 100$  and negligible leakage current, if  $V_{CC} = 20V$ ,  $V_{EE} = 5V$ ,  $R_E = 100\Omega$ ,  $R_C = 2 k\Omega$  and  $I_C = 6mA$ 



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- (a) (i) Execution of a sequence of instruction of a program involves 200 instruction fetch operations, 100 memory operand read operations, and 80 memory operand write operations. Find the average memory access time in executing this sequence of instructions if the memory access time is 2ns for a read operation with a hit in cache, 5 ns for a read operation with a miss in cache, 3 ns for a write operation with a hit in cache and 10 ns for a write operation with a miss in cache. The cache hit ratio is 0.9. Consider the time taken for fetch operation to be equal to that of read operation.
  - (ii) Derive the expression for the number of comparisons required in the worst case for sorting an array of 'n' elements using Bubble sort. Calculate it for any array of 100 elements. When will such maximum number comparisons occur in Bubble sort?

[20 Marks]

(b) A spring controlled, electrodynamic voltmeter has a range of 100V, has a square law scale response, and it takes 0.08A on d.c for full scale deflection of 120°. The control constant is  $1 \times 10^{-6}$  N.m/degree. The true potential different across the instrument is 100.42 V, when it reads 100V at 50 Hz. Determine the initial mutual inductance of the instrument.

[20 Marks]

(c) (i) Find  $I_{C1}$  in the circuit shown in the figure below. Assume that the two transistors are matched and  $V_{BE} = 0.7V, \beta = 100.$  10V



(ii) Find the feedback factor  $\beta$  of the negative feedback network required for an amplifier with open loop gain  $A_0 = 2000 \pm 200$  to reduce the variation to less than  $\pm 0.2\%$ . Find the overall gain of the system with feedback.

[20 Marks]



8. (a) Name the layers of Open Systems Interconnection (OSI) model created by the International Organisation for Standardisation for different computer systems to communicate with each other using standard protocols. Mention the important functions of each of these layers in brief.

[20 Marks]

(b) In an oscilloscope the deflection factor of CRT is 80 V/cm and the accelerating voltage is 2500V. What is the minimum distance required from centre of deflection plates to screen that allows full deflection of 4 cm on the oscilloscope screen?

[20 Marks]

(c) Derive the expression for the frequency and the condition for starting of sustained oscillation in a transistorized R-C phase shift oscillator. Neglect  $h_{oe}$  and  $h_{re}$ . Assume  $R >> h_{ie}$  and a load resistance  $R_{L}$  is a.c. coupled to the oscillator.

[20 Marks]